



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Joseph A. Urbano

Appln. No.: 09/396,486

Filing Date: September 14, 1999

Title: MEDICAL DIAGNOSTIC
ULTRASOUND SYSTEM AND
METHOD

Examiner: Francis J. Jaworski

Art Unit: 3737

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Date of Deposit or Transmission: Sept. 23, 2005By: Sara E. Armstrong
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Alexandria, VA 22313-1450INFORMATION DISCLOSURE STATEMENT

Sir:

This Information Disclosure Statement is submitted:

- ☐ under 37 CFR 1.97 (b),
(Within three months of either ☐ the filing date of a national application other than a continued prosecution application under 37 CFR 1.53(d) or ☐ the date of national stage entry of international application; or ☐ before mailing date of first office action on the merits; or ☐ before mailing date of a first office action after filing of a request for continued examination under 37 CFR 1.114).
- ☐ under 37 CFR 1.97 (c) together with either a:
☐ Certification under 37 CFR 1.97 (e), OR
☐ the \$ 180.00 fee under 37 CFR 1.17 (p) authorized to be charged to Deposit Account 19-2179. At any time during the pendency of this application, please charge any fees required or credit any overpayment to Deposit Account 19-2179 pursuant to 37 CFR 1.25.
(After the 37 CFR 1.97 (b) time period, but before mailing date of any of ☐ a final action under 37 CFR 1.113, or ☐ a notice of allowance under 37 CFR 1.311, or ☐ an action that otherwise closes prosecution).
- ☒ under 37 CFR 1.97 (d) together with a:
☒ Certification under 37 CFR 1.97 (e), AND
☒ the \$ 180.00 fee under 37 CFR 1.17 (p) authorized to be charged to Deposit Account 19-2179. At any time during the pendency of this application, please charge any fees required or credit any overpayment to Deposit Account 19-2179 pursuant to 37 CFR 1.25.
(After the 37 CFR 1.97 (c) time period, but ☐ on or ☐ before payment of the issue fee).
- ☐ under 37 CFR 1.97 (i)
(Filed after time period of 37 CFR 1.97 (d) or not in compliance with 37 CFR 1.98 - No fee or certification required; references to be placed in file but not considered).

Applicant(s) submit herewith Form PTO 1449 - Information Disclosure Citation which applicant(s) believe(s) may be material to the examination of this application and for which there may be a duty to disclose in accordance with 37 CFR 1.56,
☒ together with hard copies of all references as this application was filed before June 30, 2003; or
☐ with hard copies of only non-U.S. patents or non-U.S. published patent applications or U.S. pending unpublished patent applications (or portion thereof) or publications or other information of which applicant(s) are aware and which should be made of record in the above-identified application; or
☐ without hard copies of publications or other information which have been submitted previously in the parent application (application no. _____) to which the above-identified application claims priority under 35 USC 120 and which should be made of record in the above-identified application.

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37 CFR 1.97(e) Certifications:

☐ The undersigned hereby certifies under **37 CFR 1.97(e)(1)** that each item of information contained in the information disclosure statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement; **OR**

☒ The undersigned certifies under **37 CFR 1.97(e)(2)** that no item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the undersigned after making reasonable inquiry, no item of information contained in the information disclosure statement was known to any individual designated in 37 CFR 1.56(c) more than three months prior to the filing of the information disclosure statement.

☐ A concise explanation of the relevance of foreign language patents, foreign language publications and other foreign language information listed on PTO Form 1449, as presently understood by the individual(s) designated in 37 CFR 1.56 (c) most knowledgeable about the content that is given on ☐ the attached separate sheet or ☐ incorporated in the applicant's specification or ☐ by the enclosed English-language translation or portion thereof within the possession, custody, or control of, or is readily available to any individual designated in 37 CFR 1.56 (c).

☐ Applicant does not believe any fee is due. However, at any time during the pendency of this application, please charge any fees required or credit any overpayment to Deposit Account **19-2179** pursuant to 37 CFR 1.25. A duplicate copy of this sheet is enclosed.

The filing of this information disclosure statement shall not be construed as a representation that a search has been made or that no other material information exists. Further, the filing of this information disclosure statement shall not be construed as an admission against interest in any manner or as an admission that the information cited is or is considered to be material to patentability.

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Date: 9/23/05



Substitute for Form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Complete if Known	
				Application Number	09/396,486
				Filing Date	Sept. 14, 1999
				First Named Inventor	Joseph A. Urbano
				Group Art Unit	3737
				Examiner Name	Francis J. Jaworski
Sheet	1	of	1	Attorney Docket Number	1999P82189US

OTHER NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		Barr, Michael. "How Programmable Logic Works." <u>Netrino</u> . 24 Mar. 2004. Netrino, LLC. 21 Sep. 2005 < http://www.netrino.com/Articles/ProgrammableLogic/ >	
		Barr, Michael. "Programmable Logic: What's It To Ya?." 1999. Embedded Systems Programming. 22 Sep. 2005 < http://www.embedded.com/1999/9906/9906sr.htm >	
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		Jacobson, Neil G. "Internet Reconfigurable Logic Leveraging PLDs to Enhance Embedded System Functionality." <u>TechOnLine</u> . 10 Dec 1999. Xilinx. 22 Sep. 2005 < http://www.techonline.com/community/ed_resource/feature_article/3454 >	
		"PLD." PLD Definition. 2004. Computer Desktop Encyclopedia. 22 Sep. 2005 < http://www.eweek.com/encyclopedia_term/0,2542,t=PLD&i=49377,00.asp >.	

Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹Unique citation designation number. ²Applicant is to place a check mark here if English language Translation is attached.